





January 2007



- Pletronics' LV99D Series is a quartz crystal controlled precision square wave generator with an LVDS output.
- The package is designed for high density surface mount designs.
- · Low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 5 x 7 mm LCC Ceramic Package
- Enable/Disable Function on pad 1
- Output frequency is synthesized.
- Low Jitter
- RoHS 6/6 Compliant

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.16 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020C

Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{cc} Supply Voltage	-0.5V to +5.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V

Thermal Characteristics

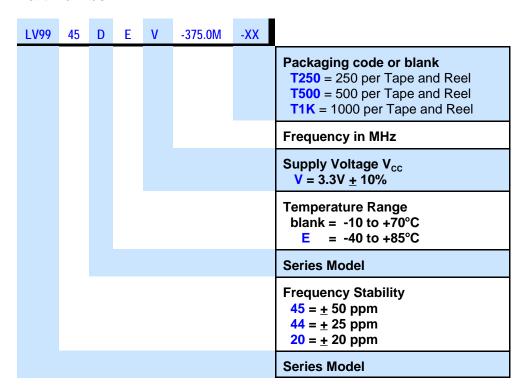
The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



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Part Number:



Part Marking:

PLE LV99 Marking Legend:

FF.FFF M PLE = Pletronics

• YMDXX FF.FFF M = Frequency in MHZ

YMD = Date of Manufacture (year-month-day)
All other marking is internal factory codes

Codes for Date Code YMD

Code	7	8	9	0	1	2
Year	2007	2008	2009	2010	2011	2012

Code	Α	В	С	D	E	F	G	H	7	K	L	M
Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	Α	В	С
Day	1	2	3	4	5	6	7	8	9	10	11	12
Code	D	Е	F	G	Н	J	K	L	М	N	Р	R
Day	13	14	15	16	17	18	19	20	21	22	23	24
Code	Т	U	٧	W	Х	Y	Z					
Day	25	26	27	28	29	30	31					



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Electrical Specification for 3.30V $\pm 10\%$ over the specified temperature range and the frequency range of 10.9 MHz to 670 MHz

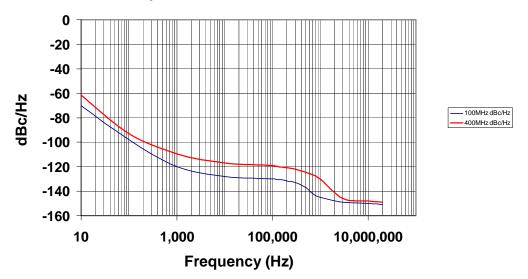
Item	Min	Max	Unit	Condition
Frequency Accuracy "45"	-50	+50	ppm	For all supply voltages, load changes, aging for 1
"44"	-25			year, shock, vibration and temperatures
"20"	-20	+20		
Output Waveform		LVDS		
Output High Level		1.60	Volts	
Output Low Level	0.90		Volts	
Differential Output (V _{OD})	250	450	mVolts	See load circuit
Output Offset Voltage (Vos)	1.125	1.375	Volts	R1 = 50 ohms
Differential Output Error (dV _{os})		50	mVolts	
Output Symmetry	47	53	%	Referenced to 50% of amplitude or
Output Symmetry	41	55	70	crossing point
Output T _{RISE} and T _{FALL}	150	230	pS	Vth is 20% and 80% of waveform
Jitter	-	0.6	pS RMS	Measured from 12KHz to 20MHz from Fnominal
	-	2.8		Measured from 10Hz to 20MHz from Fnominal
Output Short Circuit Current	-	-20	mA	Vout = 0.0V
Vcc Supply Current	-	80	mA	
Enable/Disable Internal Pull-up	50	-	Kohm	To Vcc (equivalent resistance)
V disable	_	0.8	Volts	Referenced to Ground
V enable	2.0	_	Volts	Referenced to Ground
Output leakage $V_{OUT} = V_{CC}$	-20	+20	uA	Pad 1 low, device disabled
$V_{OUT} = 0V$	-20	+20	uA	T da T lon, do lloc disabled
Enable	-	10	nS	Time for output to reach a logic state
Disable time	-	10	nS	Time for output to reach a high Z state
Start up time	-	5	mS	Measured from the time Vcc = 3.0V
Operating Temperature Range	-10	+70	°C	Standard Temperature Range
, January and State of the Stat	-40	+85	°C	Extended Temperature Range "E" Option
Storage Temperature Range	-55	+125	°C	

Specifications with Pad 1 E/D open circuit

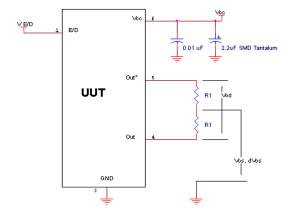


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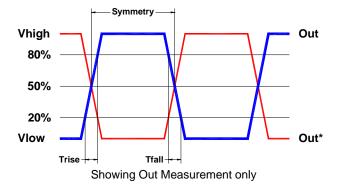
Typical Phase-Noise Response



Load Circuit



Test Waveform





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Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions		
Human Body Model	2000	MIL-STD-883 Method 3115		
Charged Device Model	1500	JESD 22-C101		

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

 Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

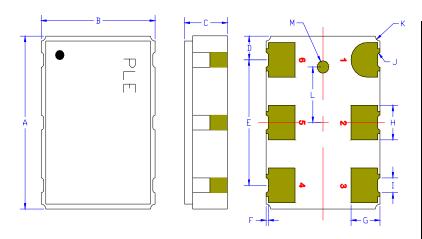
2nd LvL Interconnect Category=e4

Max Safe Temp=260C for 10s 2X Max



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Mechanical:



Contacts:

Gold 11.8 µinches 0.3 µm minimum over Nickel 50 to 350 µinches 1.27 to 8.89 µm

¹ Typical dimensions

Not to Scale

Center metalized pad on the base is not connected.

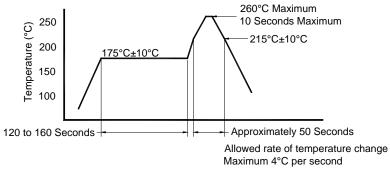
	Inches	mm
Α	0.276 <u>+</u> 0.006	7.00 <u>+</u> 0.15
В	0.197 <u>+</u> 0.006	5.00 <u>+</u> 0.15
С	0.067 max	1.70 max
D ¹	0.038	0.96
E ¹	0.200	5.08
F ¹	0.004	0.10
G¹	0.050	1.27
H¹	0.055	1.40
I ¹	0.024	0.60
J ¹	0.004r	0.10r
K ¹	0.008r	0.20r
L ¹	0.089	2.25
M¹	0.010r	0.25r

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. If <0.80 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm CC}$ if the oscillator is to be always on.
2	No connect	This pad should be connected to Ground or Supply Voltage to lower the packages thermal resistance.
3	Ground (GND)	
4	Output	The outputs must be terminated, 100 ohms between the outputs is the ideal
5	Output*	termination. Capacitor coupled terminations can be used.
6	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.



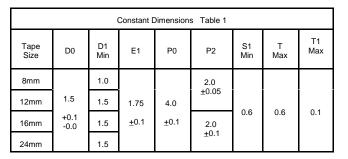
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Reflow Cycle (typical for lead free processing)



The part may be reflowed 2 times without degradation.

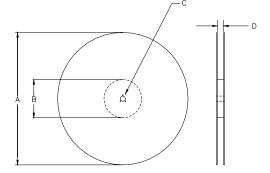
Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

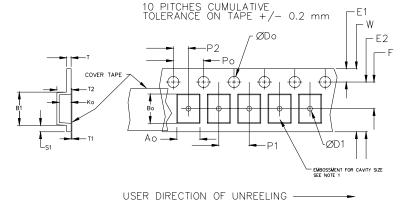


Variable Dimensions Table 2								
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko	
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1	

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm Not to scale





		REE	L DIMENSI	ONS	
Α	inches	7.0	10.0	13.0	
	mm	177.8	254.0	330.2	
В	inches	2.50	4.00	3.75	
	mm	63.5	101.6	95.3	Tape Width
С	mm	13	widiii		
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0

Reel dimensions may vary from the above



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